

CCCCCCCCCCCC	000000000	NNN	NNN	WWW	WWW	
CCCCCCCCCCCC	000000000	NNN	NNN	WWW	WWW	
CCCCCCCCCCCC	000000000	NNN	NNN	WWW	WWW	
CCC	000	000	NNN	NNN	WWW	WWW
CCC	000	000	NNN	NNN	WWW	WWW
CCC	000	000	NNN	NNN	WWW	WWW
CCC	000	000	NNNNNN	NNN	WWW	WWW
CCC	000	000	NNNNNN	NNN	WWW	WWW
CCC	000	000	NNNNNN	NNN	WWW	WWW
CCC	000	000	NNN NNN	NNN	WWW	WWW
CCC	000	000	NNN NNN	NNN	WWW	WWW
CCC	000	000	NNN NNN	NNN	WWW	WWW
CCC	000	000	NNN NNN	NNN	WWW	WWW
CCC	000	000	NNN NNN	NNN	WWW	WWW
CCC	000	000	NNN NNN	NNN	WWW	WWW
CCC	000	000	NNN NNN	NNN	WWW	WWW
CCC	000	000	NNN NNN	NNN	WWW	WWW
CCC	000	000	NNN NNN	NNN	WWW	WWW
CCC	000	000	NNN NNN	NNN	WWW	WWW
CCC	000	000	NNN NNN	NNN	WWW	WWW
CCC	000	000	NNN NNN	NNN	WWW	WWW
CCC	000	000	NNN NNN	NNN	WWW	WWW
CCC	000	000	NNN NNN	NNN	WWW	WWW
CCC	000	000	NNN NNN	NNN	WWW	WWW
CCC	000	000	NNN NNN	NNN	WWW	WWW
CCCCCCCCCCCC	000000000	NNN	NNN	WWW	WWW	
CCCCCCCCCCCC	000000000	NNN	NNN	WWW	WWW	
CCCCCCCCCCCC	000000000	NNN	NNN	WWW	WWW	

FILEID**CONVLINK

CCCCCCCC 000000 NN NN VV VV LL
CCCCCCCC 000000 NN NN VV VV LL
CC 00 00 NN NN VV VV LL
CC 00 00 NNNN NN VV VV LL
CC 00 00 NNNN NN VV VV LL
CC 00 00 NN NN VV VV LL
CC 00 00 NN NN VV VV LL
CC 00 00 NN NNNN VV VV LL
CC 00 00 NN NNNN VV VV LL
CC 00 00 NN NN VV VV LL
CC 00 00 NN NN VV VV LL
CCCCCCCC 000000 NN NN VV LLLLLLLL
CCCCCCCC 000000 NN NN VV LLLLLLLL

A 10x10 grid of characters where 'R' and 'E' are black and 'S', 'V', 'E', and 'S' are white. The pattern reads 'REVERSE' horizontally across the grid. The 'R's are in the first and ninth columns, 'E's are in the second and eighth columns, and the other characters are in the fifth, sixth, seventh, and tenth columns.

TITLE 'VAX-11 CONVERT'

Version: 'V04-000'

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Facility: VAX-11 CONVERT

Abstract: Linkage definitions for the convert utilities

Environment:

VAX/VMS Operating System

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Author: Keith B Thompson Creation date: May-1982

Modified by:

V03-002 KBT0477 Keith B. Thompson 29-Jan-1983
Add init_fast_load, load_secondary, extend_area,
check_key and load_key linkage

V03-001 KBT0437 Keith B. Thompson 16-Dec-1982
Preserve R1 in get_vm and get_tmp_vm calls

Linkages NOTE: By only using reg. 2-11 a normal JSB or CALLx link will preserve these registers.

Linkage naming convention:

General Convert linkage:	CLSlinkage
Reclaim specific linkage:	RLSlinkage
Add_key specific linkage:	ALSlinkage
Specific Routine Linkage:	CLSroutine_name, RLSroutine_name or ALSroutine_name

Registers:

<u>_REG_11</u>	11 - KEY_DESC
<u>_REG_10</u>	11 - KEY_DESC 10 - CTX
<u>_REG_9</u>	11 - KEY_DESC 10 - CTX 9 - BUCKET
<u>_REG_8</u>	11 - KEY_DESC 10 - CTX 9 - BUCKET 8 - RECORD_CTRL (CL link) KEY_POINTER (RL link)

LINKAGE

```
| Generic jsb link
| CL$JSB_LINK = JSB,
| CL$JSB_LINK_ARG1 = JSB ( REGISTER = 1 ),
| CL$JSB_LINK_ARG2 = JSB ( REGISTER = 1,REGISTER = 2 ),
| Convert
| CL$JSB_REG_11 = JSB : GLOBAL( KEY_DESC = 11 ),
| CL$JSB_REG_10 = JSB : GLOBAL( CTX KEY_DESC = 10,
|                               KEY_DESC = 11 ),
| CL$JSB_REG_9 = JSB : GLOBAL( BUCKET
|                             CTX = 9,
|                             KEY_DESC = 10,
|                             KEY_DESC = 11 ),
| CL$JSB_REG_8 = JSB : GLOBAL( RECORD_CTRL
|                             BUCKET = 8,
|                             BUCKET = 9,
```

CTX
KEY_DESC = 10,
 = 11),

! Reclaim linkages

RLSJSB_REG_9 = JSB : GLOBAL(BUCKET
 CTX = 9,
 KEY_DESC = 10,
 = 11),

RLSJSB_REG_8 = JSB : GLOBAL(KEY_POINTER
 BUCKET = 8,
 CTX = 9,
 KEY_DESC = 10,
 = 11),

! Add_key linkages

ALSJSB_REG_11 = JSB : GLOBAL(KEY_DESC = 11),

! Routine specific links

CL\$ALLOCATE_CTX	= JSB : GLOBAL(CTX = 10),	
CL\$COMPARE_KEY	= JSB (REGISTER = 0, REGISTER = 1, REGISTER = 2, REGISTER = 3),	Segment type Segment size New key seq. Old key seq.
CL\$CONVERT_VBN_ID	= JSB : GLOBAL(SORT_VBN = 6, SORT_ID = ?),	
CL\$COPY_KEY	= JSB : GLOBAL(RECORD_CTRL = 8, CTX = 10, KEY_DESC = 11),	
CL\$CVT_TO_ASC	= JSB,	
CL\$EXTEND_AREA	= JSB,	
CL\$FAST_LOAD	= JSB : GLOBAL(KEY_DESC = 11),	
CL\$FREE_VM	= JSB,	
CL\$FREE_TEMP_VM	= JSB,	
CL\$GET_NEXT_KEY	= JSB : GLOBAL(KEY_DESC = 11),	
CL\$GET_RECORD	= JSB,	
CL\$GET_TEMP_VM	= JSB : PRESERVE (1),	
CL\$GET_VM	= JSB : PRESERVE (1),	
CL\$INIT_FAST_LOAD	= JSB : GLOBAL(BUCKET = 9, CTX = 10, KEY_DESC = 11),	
CL\$LOAD_SECONDARY	= JSB : GLOBAL(BUCKET = 9, CTX = 10, KEY_DESC = 11),	
CL\$LOAD_SDIR	= JSB : GLOBAL(CTX = 10),	
CL\$READ_PROLOGUE	= JSB,	
CL\$SET_KEY_DESC	= JSB : GLOBAL(KEY_DESC = 11),	
CL\$SORT_PRIMARY	= JSB : GLOBAL(KEY_DESC = 11),	
CL\$SORT_SECONDARY	= JSB : GLOBAL(KEY_DESC = 11),	
CL\$WRITE_AREA_DESC	= JSB (REGISTER = 7),	
CL\$WRITE_KEY_DESC	= JSB : GLOBAL(KEY_DESC = 11),	
AL\$CHECK_KEY	= JSB : GLOBAL(KEY_DESC = 11),	
AL\$LOAD_KEY	= JSB : GLOBAL(KEY_DESC = 11);	

! Global register definitions

MACRO

! Global registers

DEFINE_KEY_DESC_GLOBAL	= GLOBAL REGISTER KEY_DESC	= 11 : REF BLOCK [,BYTE]%.
DEFINE_CTX_GLOBAL	= GLOBAL REGISTER CTX	= 10 : REF BLOCK [,BYTE]%.
DEFINE_BUCKET_GLOBAL	= GLOBAL REGISTER BUCKET	= 9 : REF BLOCK [,BYTE]%.
DEFINE_KEY_POINTER_GLOBAL	= GLOBAL REGISTER KEY_POINTER	= 8%.
DEFINE_RECORD_CTRL_GLOBAL	= GLOBAL REGISTER	

RECORD_CTRL = 8 : REF BLOCK [,BYTE]%,
= 6
= 7%,
DEFINEREGISTER
= EXTERNAL REGISTER
CTX : REF BLOCK [,BYTE]%,
= EXTERNAL REGISTER
BUCKET : REF BLOCK [,BYTE]%,
= EXTERNAL REGISTER
KEY_DESC : REF BLOCK [,BYTE]%,
= EXTERNAL REGISTER
KEY_POINTER : REF BLOCK [,BYTE]%,
= EXTERNAL REGISTER
RECORD_CTRL : REF BLOCK [,BYTE]%;

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VAX/VMS V4.0

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